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APPLICATION NUMBER	FILING or 371(c) DATE GRP ART U	VIT FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
10/551,745	03/19/2007	1520	I431.131.101/FIN 421 PCT/	14	2

CONFIRMATION NO. 5789

FILING RECEIPT

25281 DICKE, BILLIG & CZAJA FIFTH STREET TOWERS 100 SOUTH FIFTH STREET, SUITE 2250 MINNEAPOLIS. MN55402

Date Mailed: 07/31/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filling Receipt, please write to the Office of Initial Patent Examination's Filling Receipt Corrections. Please provide a copy of this Filling Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filling Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filling Receipt incorporating the requested corrections

Applicant(s)

Edmund Riedl, Obertraubling-Gebelkofen, GERMANY:

Power of Attorney: The patent practitioners associated with Customer Number 45782

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/DE04/00668 03/31/2004

Foreign Applications

GERMANY 103 14 876.0 04/01/2003

If Required, Foreign Filing License Granted: 07/30/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/551.745

Projected Publication Date: 11/08/2007

Non-Publication Request: No Early Publication Request: No

Title

Method for the Multi-Stage Production of Diffusion Soldered Connections for Power Components Comprising Semiconductor Chips

Preliminary Class

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Title 37, Code of Federal Regulations, 5.11 & 5.15

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